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| | Class | Subject |
| ISSUE CLASSIFICATION | | |

PROPERTY NUMBER 09

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| O.I.P.E. | PATENT DATE |
| SCANNED <i>hp</i> <i>NSD</i> <i>QA</i> <i>FW</i> | |

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| APPLICATION NO. 09/855723 | CONT/PRIOR F | CLASS 436 716 | SUBCLASS 19 | ART UNIT 2012 2825 | EXAMINER Dimyan |
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APPLICANTS
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TITLE
System of manufacturing semiconductor integrated circuit

BEST AVAILABLE

[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) | | NOTICE OF ALLOWANCE MAILED _____ | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ | _____ (Primary Examiner) | | ISSUE FEE | |
| | _____ (Date) | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) | | ISSUE BATCH NUMBER _____ | |
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